

A DATA STORAGE SYSTEM HAVING
AN IMPROVED MEMORY CIRCUIT BOARD

ABSTRACT OF THE DISCLOSURE

5 A data storage system includes a set of storage devices, a memory circuit board
that stores a cache to temporarily store copies of data elements stored in the set of
storage devices, and a processor circuit board that operates as at least one of a front-end
interface between an external device and the cache and a back-end interface between the
cache and the set of storage devices. The memory circuit board is configured to receive,
10 from the processor circuit board, a communication that includes a script command and a
payload. The payload includes a series of individual instructions. The memory circuit
board is further configured to parse the payload to identify the series of individual
instructions in response to the script command, and to perform a series of operations in
accordance with the identified series of individual instructions.

090224-1000
FOUO